



AMENDMENTS TO THE SPECIFICATION:

**[0011]** For example, in Fig. 2A, columns C1 and C0 could be redundant columns to be used if there are defects in any of columns CN-1 through C2 (which would then represent the "primary memory region" of memory array 220 – i.e., the portion of memory array 220 in which data would be stored in the absence of array defects). In such an arrangement, replacing a defective column with column C0 would require that the flip flop in data register 241 associated with column C0 receive the data originally intended for the flip flop associated with the defective column. However, the flip-flops in a shift register (i.e., data register ~~224~~241) are hardwired in a predetermined order, and so cannot be easily reordered to incorporate the redundant column in place of the defective column.